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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT					
Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Robinson Intellectual Property Law Office, P.C. PMB 955 21010 Southbank Street Potomac Falls, VA 20165				
Signature	5.	~	<b>08</b> 0		
Date	2-20-03				
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Adele M. Stamper

Docket No. 0756-23

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Patent Applicatio	n of
-1912	

Shunpei YAMAZAKI et al.

Serial No. 09/894,125 < Filed: June 29, 2001

For: CRYSTALLINE SEMICONDUCTOR)

THIN FILM, METHOD OF FABRICATING )

THE SAME, SEMICONDUCTOR DEVICE,)

AND METHOD OF FABRICATING THE

SAME

Art Unit: 2823

Examiner: B. Kebede

## **CERTIFICATE OF MAILING**

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adul M Stan Adele M. Stamper

**AMENDMENT** 

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

able Commissioner of Patents

ngton, D.C. 20231

In response to the Official Action dated November 20, 2002, please articles and the province of Patents

| Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Patents | Commissioner of Pa above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1 and 7 as follows:

(Twice Amended) A method of manufacturing a semiconductor device 1. comprising the steps of:

forming a semiconductor film comprising silicon over a substrate;

irradiating said semiconductor film with laser light in air for crystallizing said semiconductor film;

removing a natural oxidation film formed on a surface of the semiconductor film by etching after the irradiation of the laser light; and

leveling the surface of the semiconductor film by heating after removing said natural oxidation film.